

# Communication complexity of the forge-and-lose technique

(@ secure evaluation of AES-128 and SHA-256 circuits)

**Luís Brandão<sup>+</sup>**

University of Lisbon / LaSIGE (Portugal) and Carnegie Mellon University (USA)

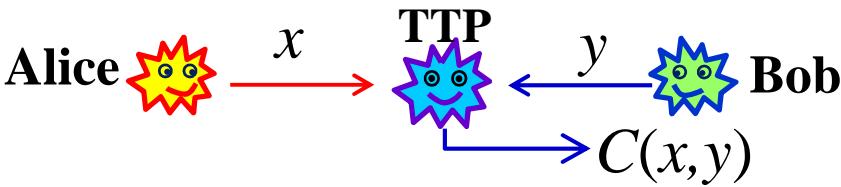
**Crypto 2013 Rump Session  
(Santa Barbara, USA, August 20)**

**1** + Ph.D. student, supported by the Portuguese Foundation for Science and Technology (FCT) through the Carnegie Mellon Portugal Program under Grant SFRH/BD/33770/2009.

# S2PC via cut-and-choose of garbled circuits

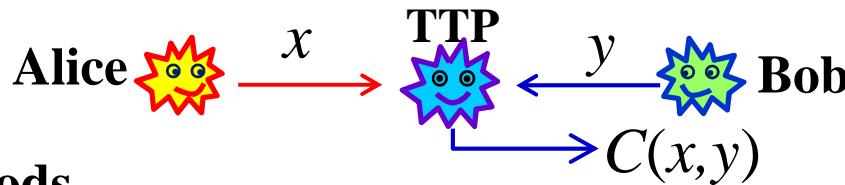
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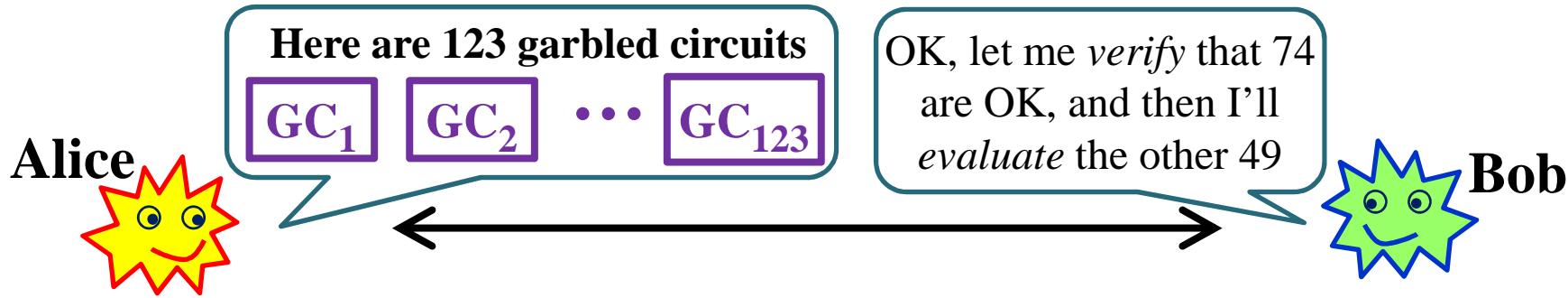


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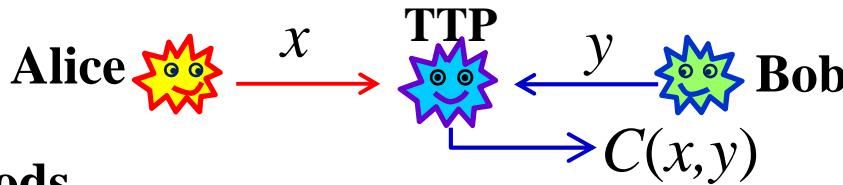


Usual C&C methods

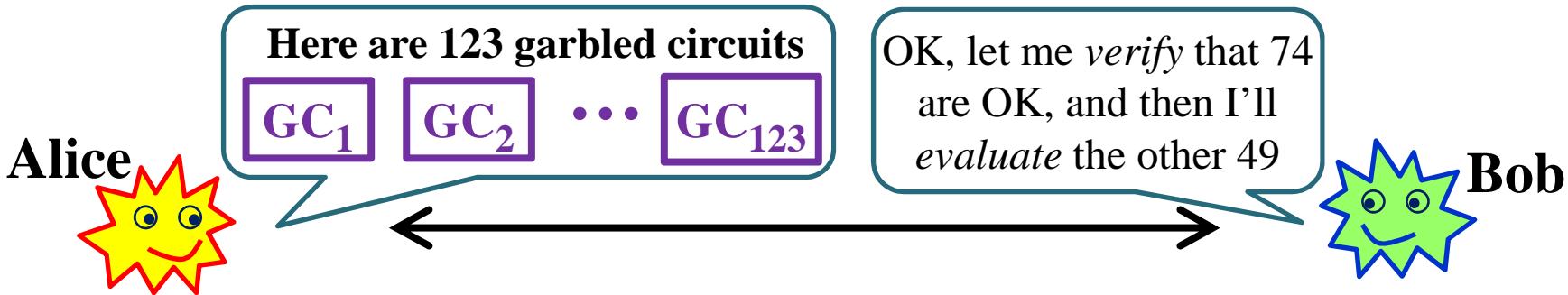


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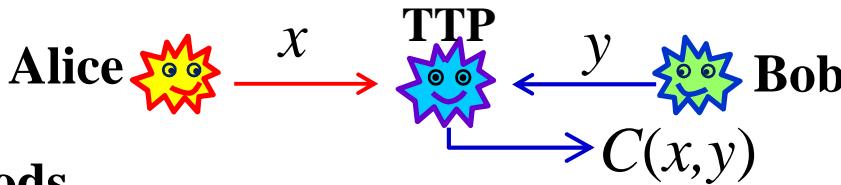
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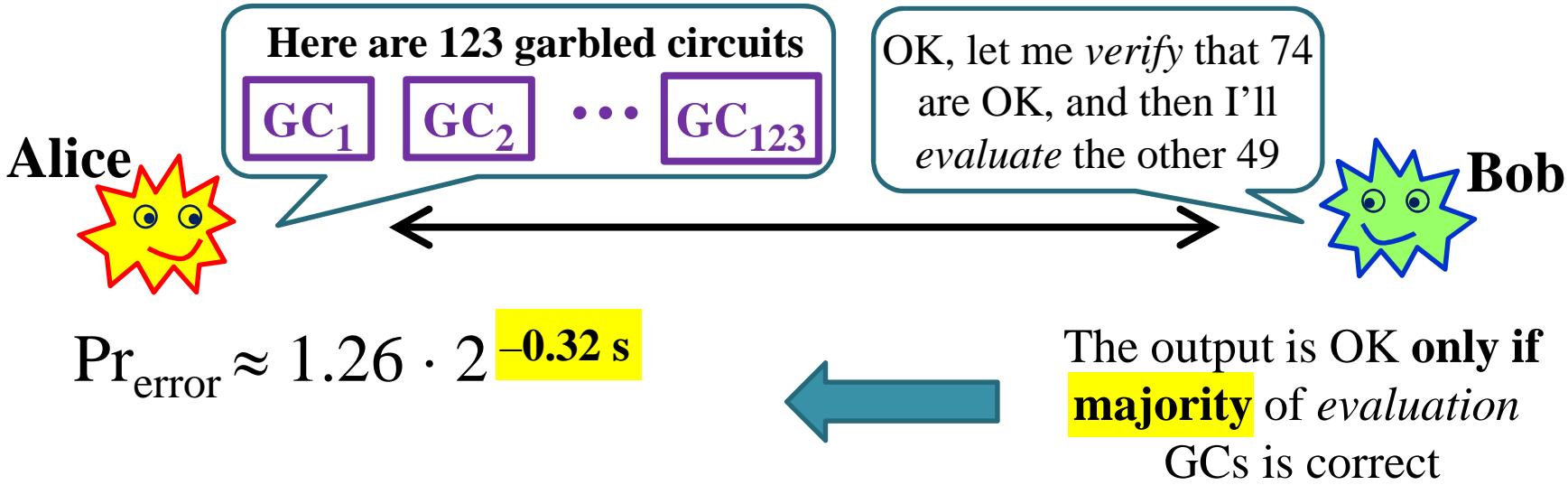
The output is **OK only if majority** of *evaluation* GCs is correct

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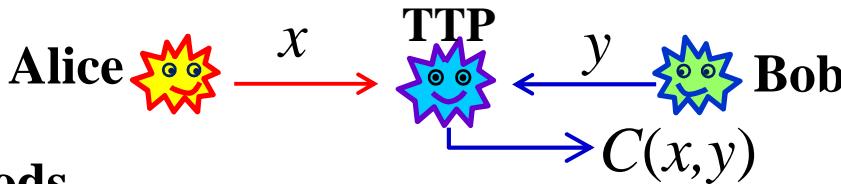


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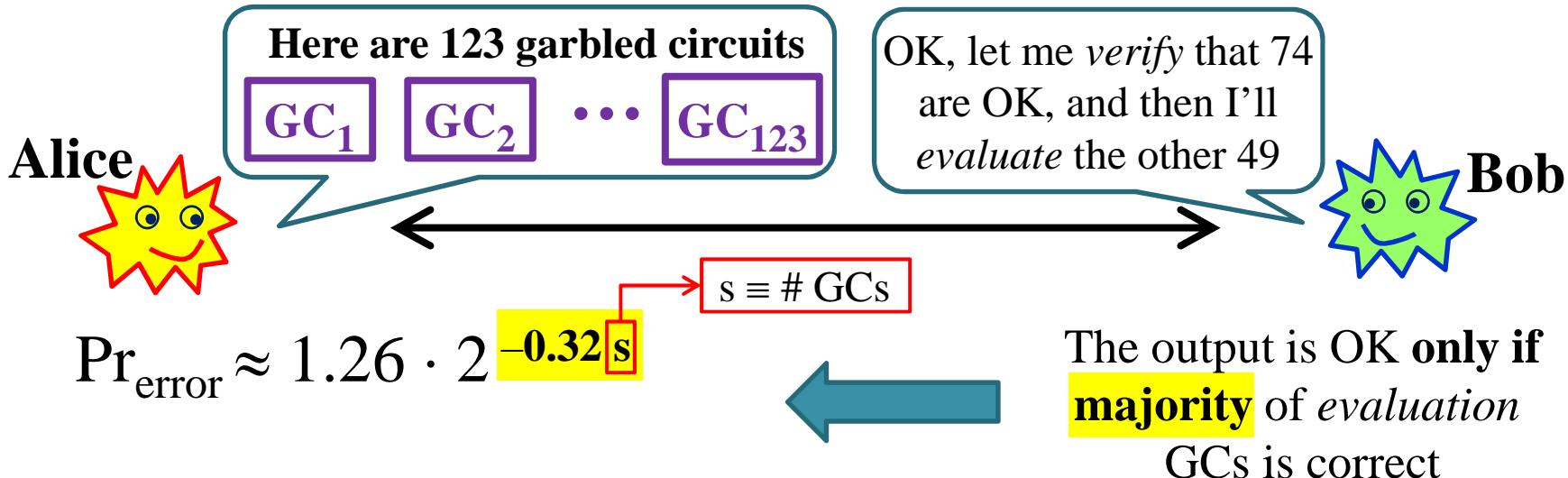


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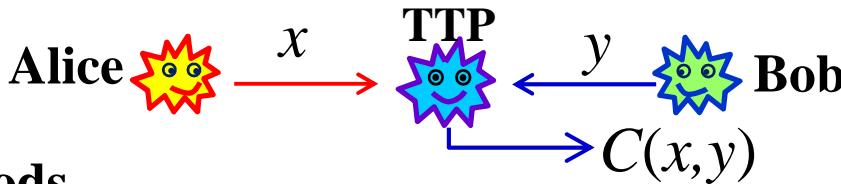


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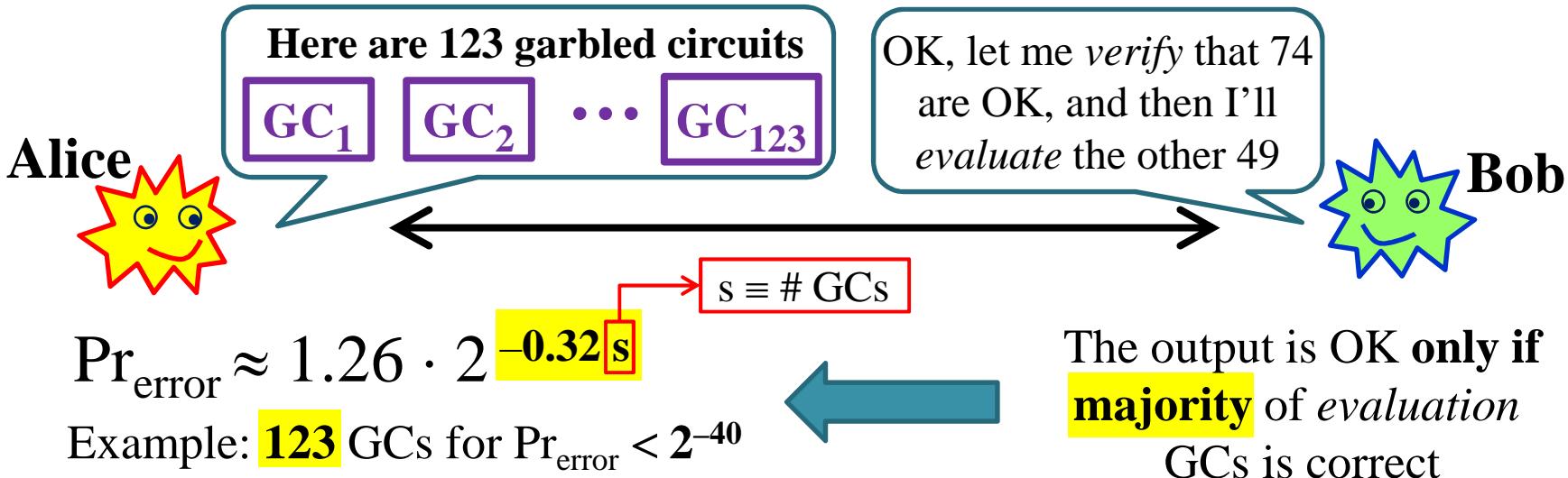


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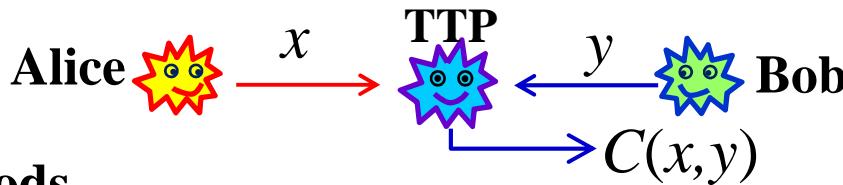


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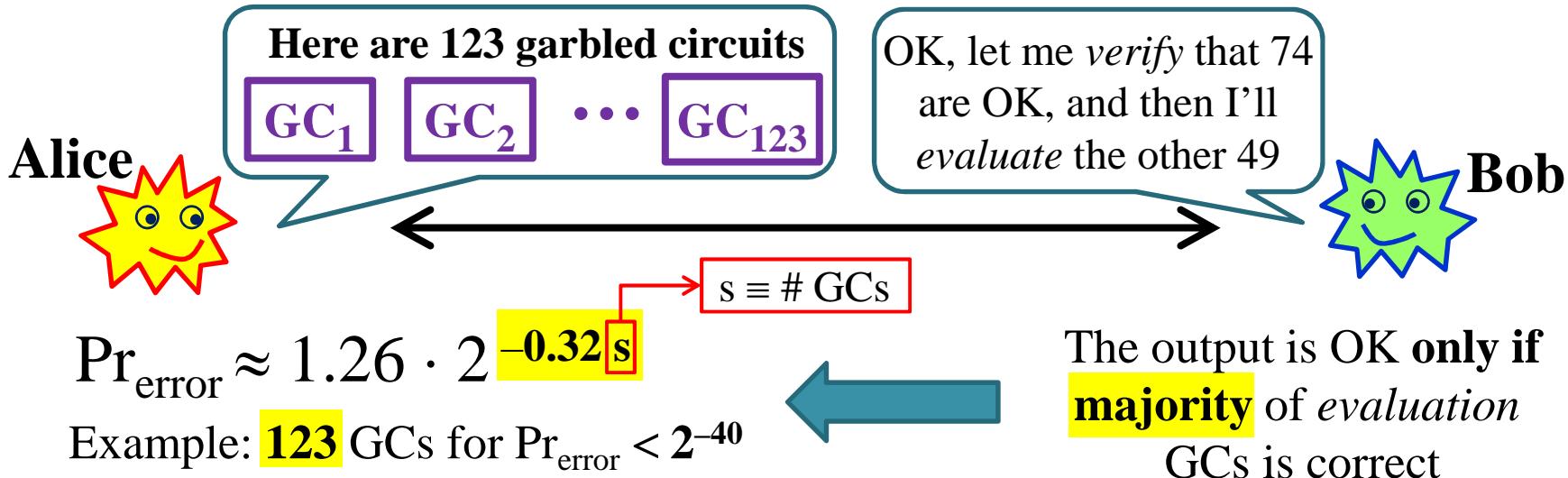


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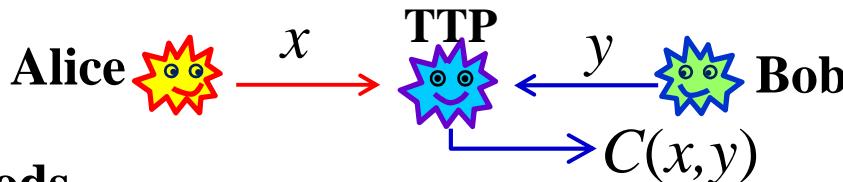


New optimal(?) C&C methods in 2013

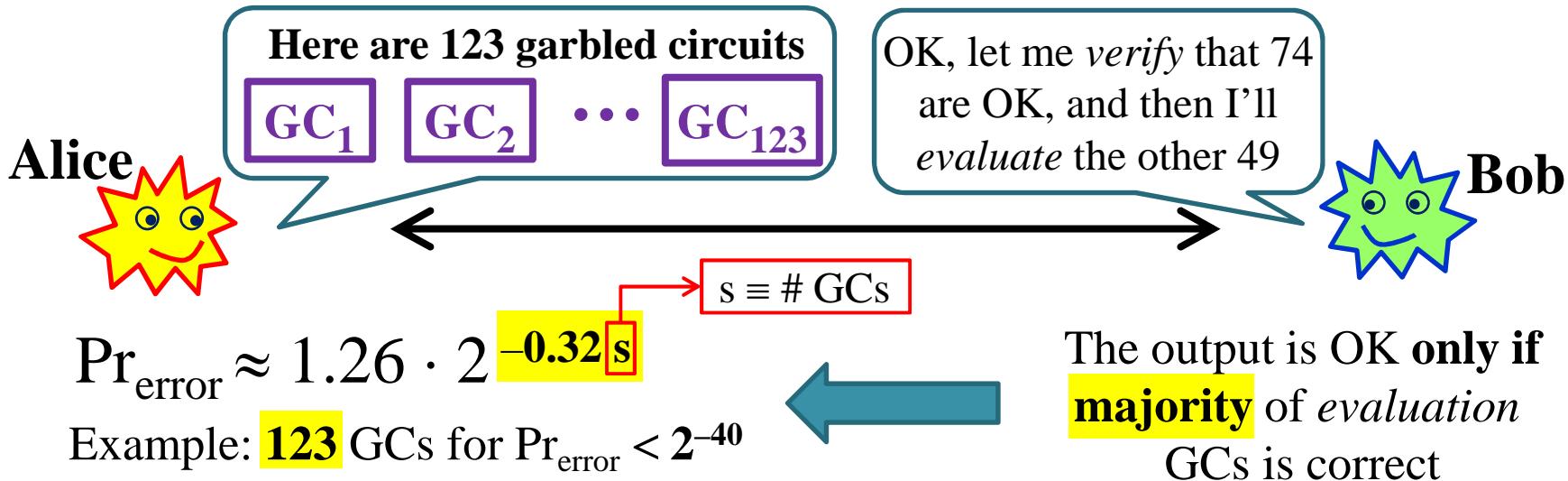
The output is OK if **at least one** *evaluation* GC is correct

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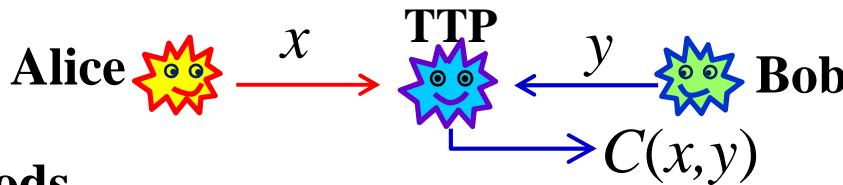
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C&C proportions ( <i>verify</i> vs. <i>evaluate</i> )	$\Pr_{\text{error}}$	# GCs: $\Pr_{\text{error}} \leq 2^{-40}$
Fixed	$\approx 1.25 \cdot 2^{-s + (\log_2 s)/2}$	44
Variable	$2^{-s}$	40

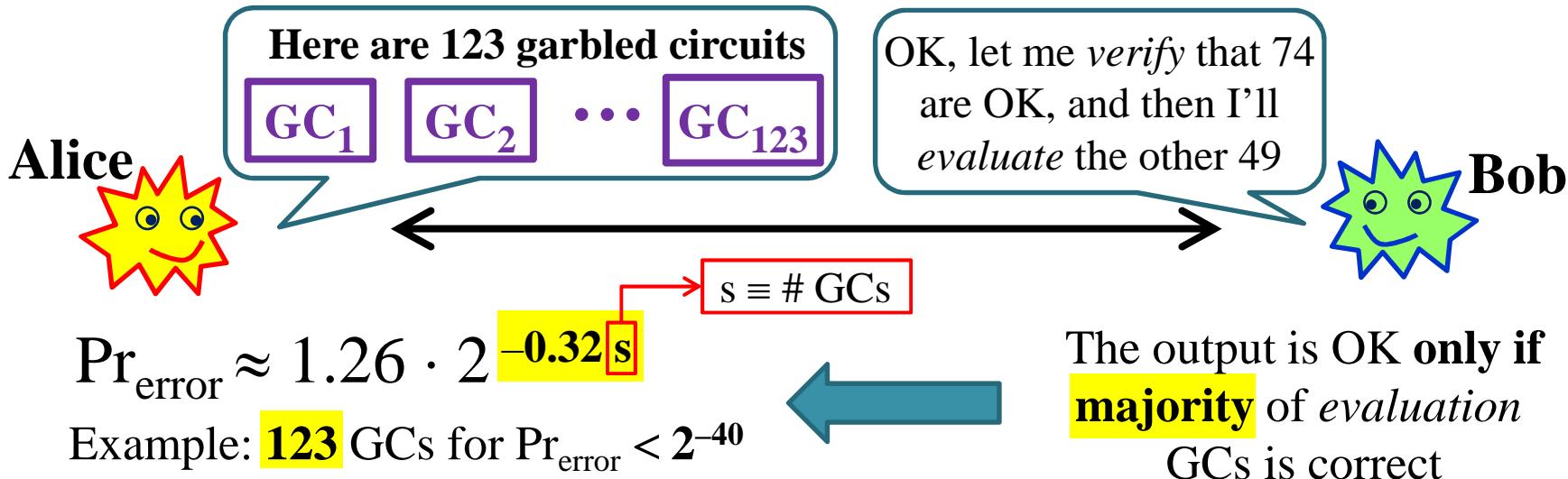
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Compare against 123

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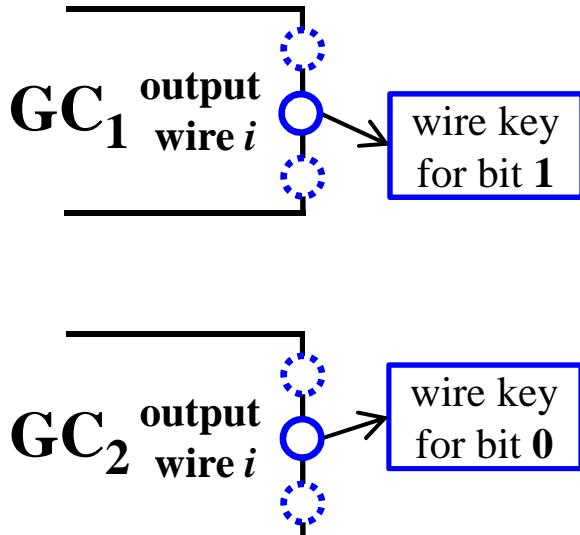
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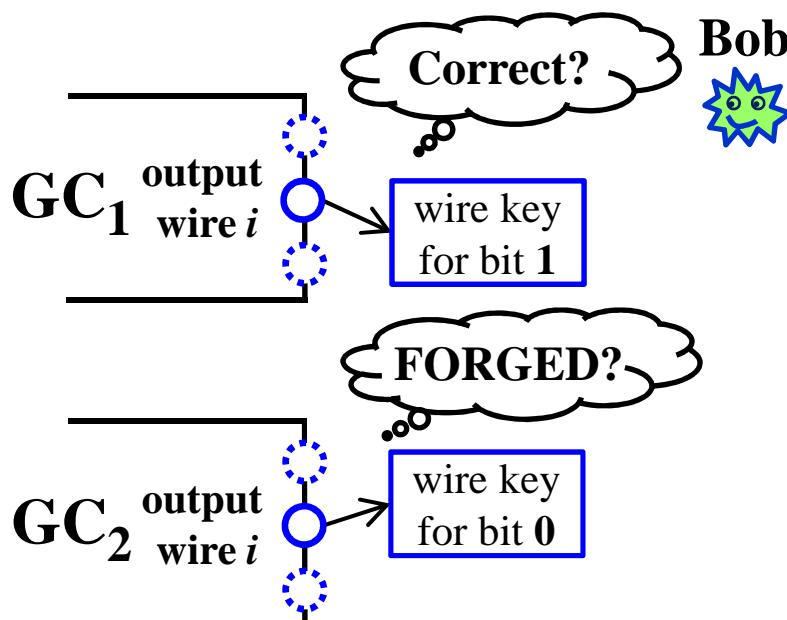
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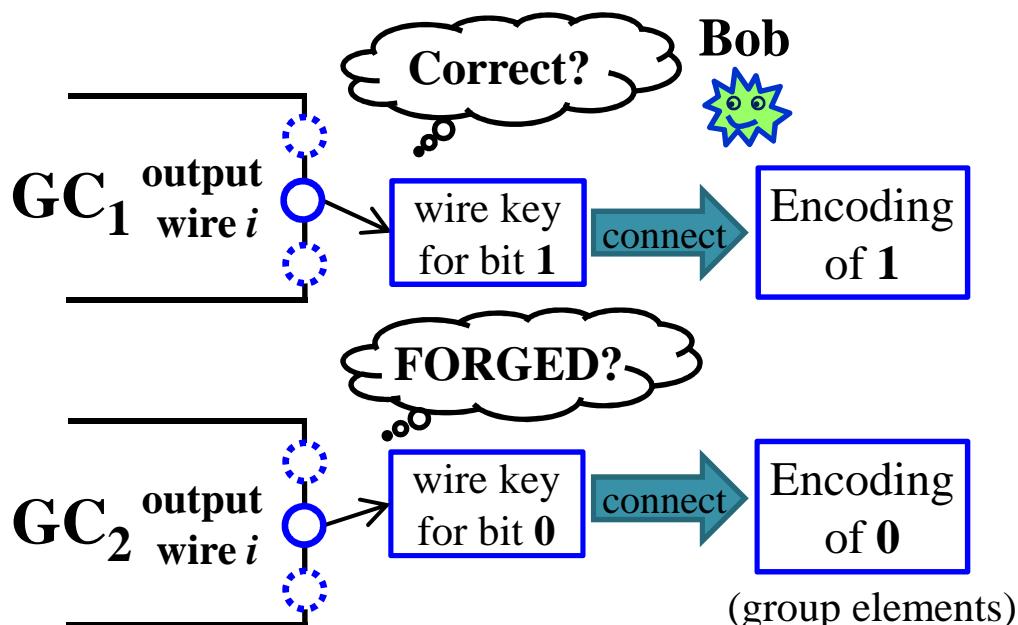
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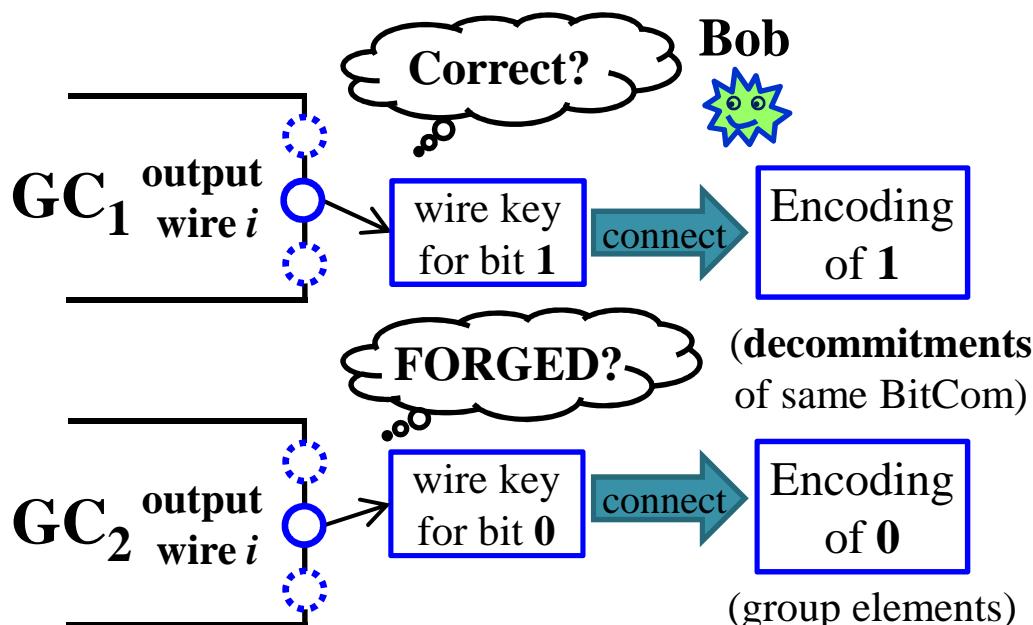
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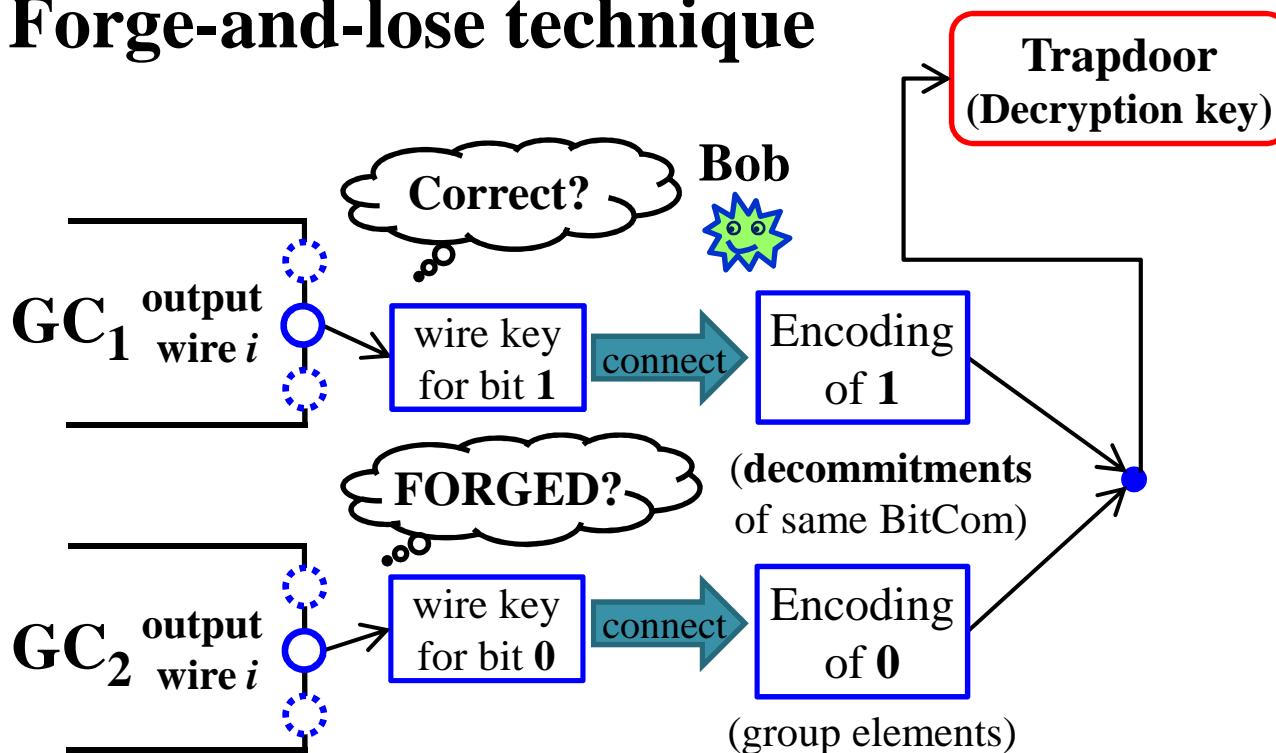
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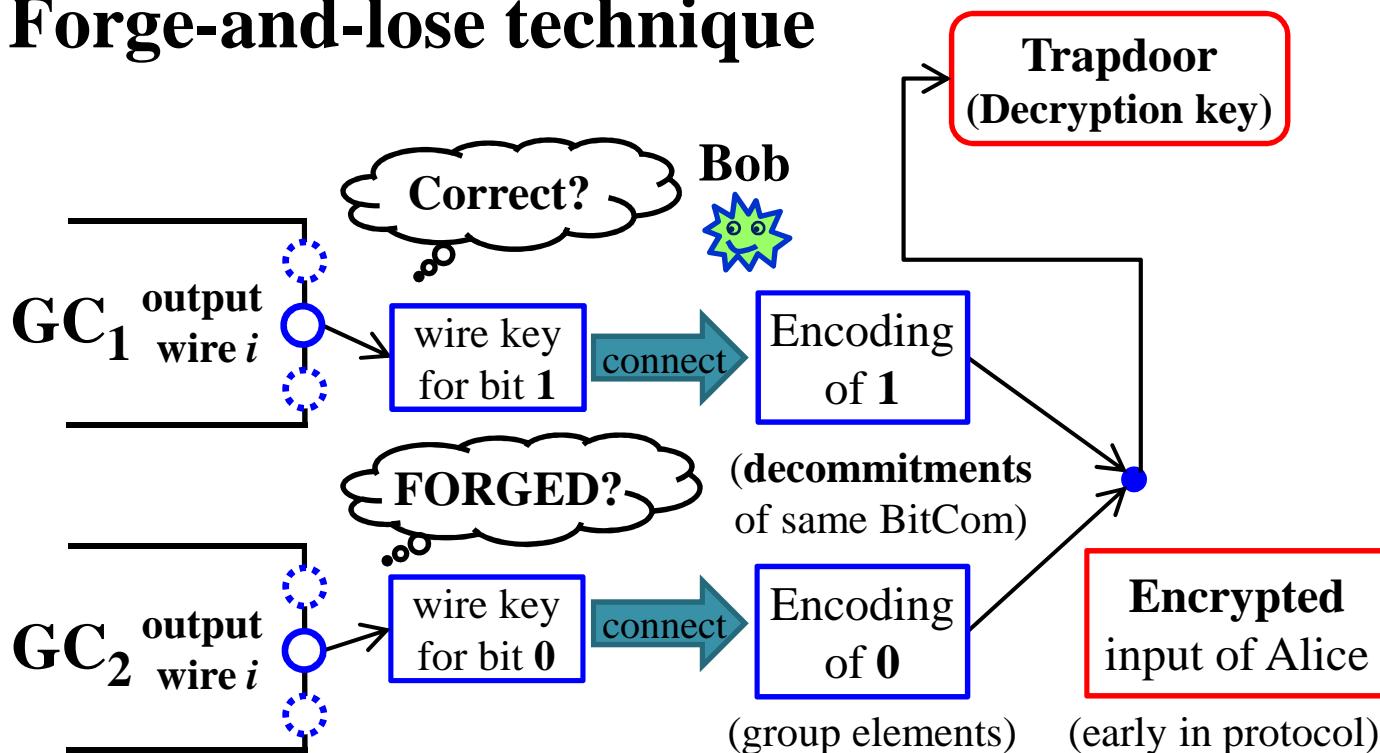
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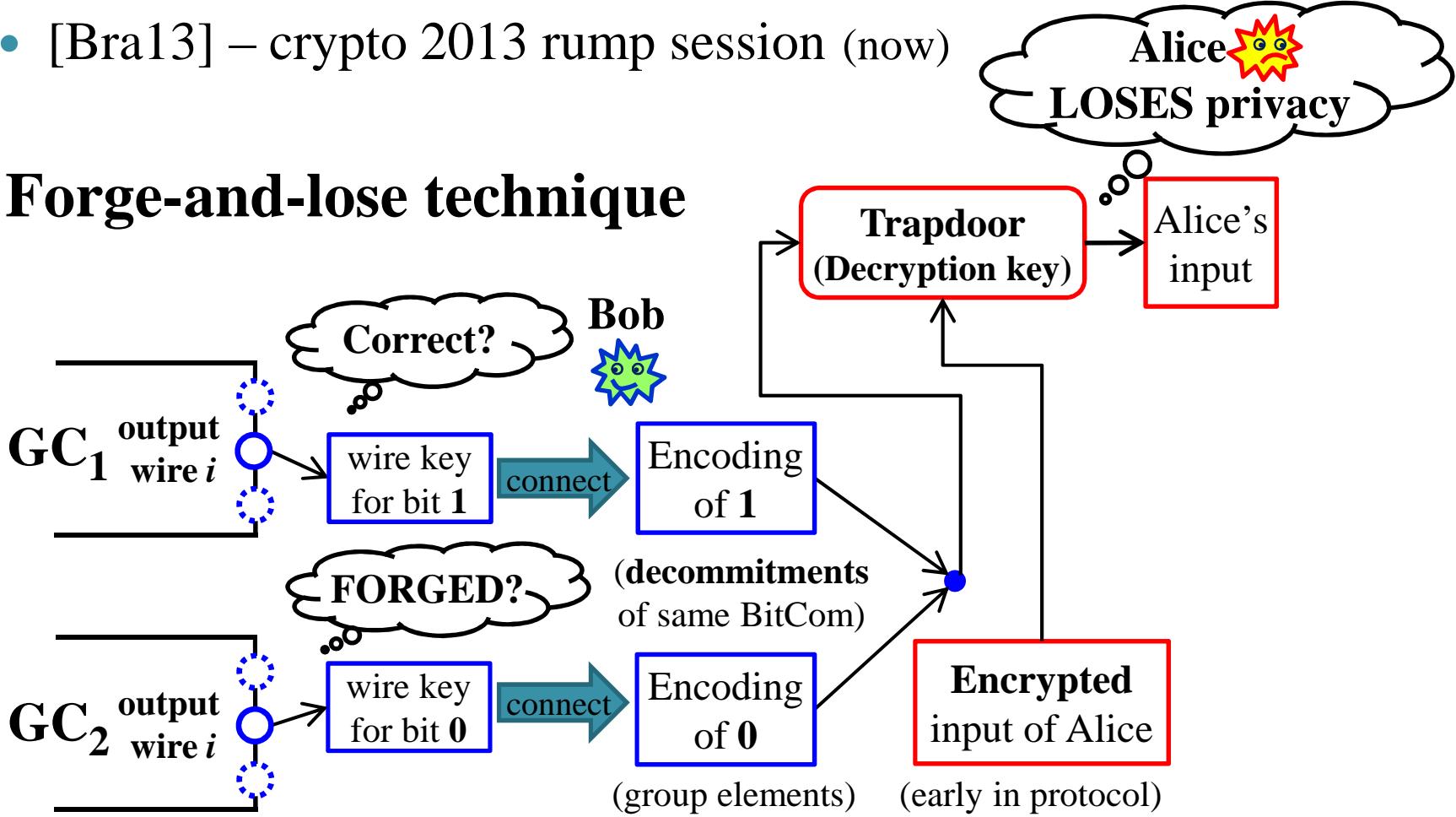
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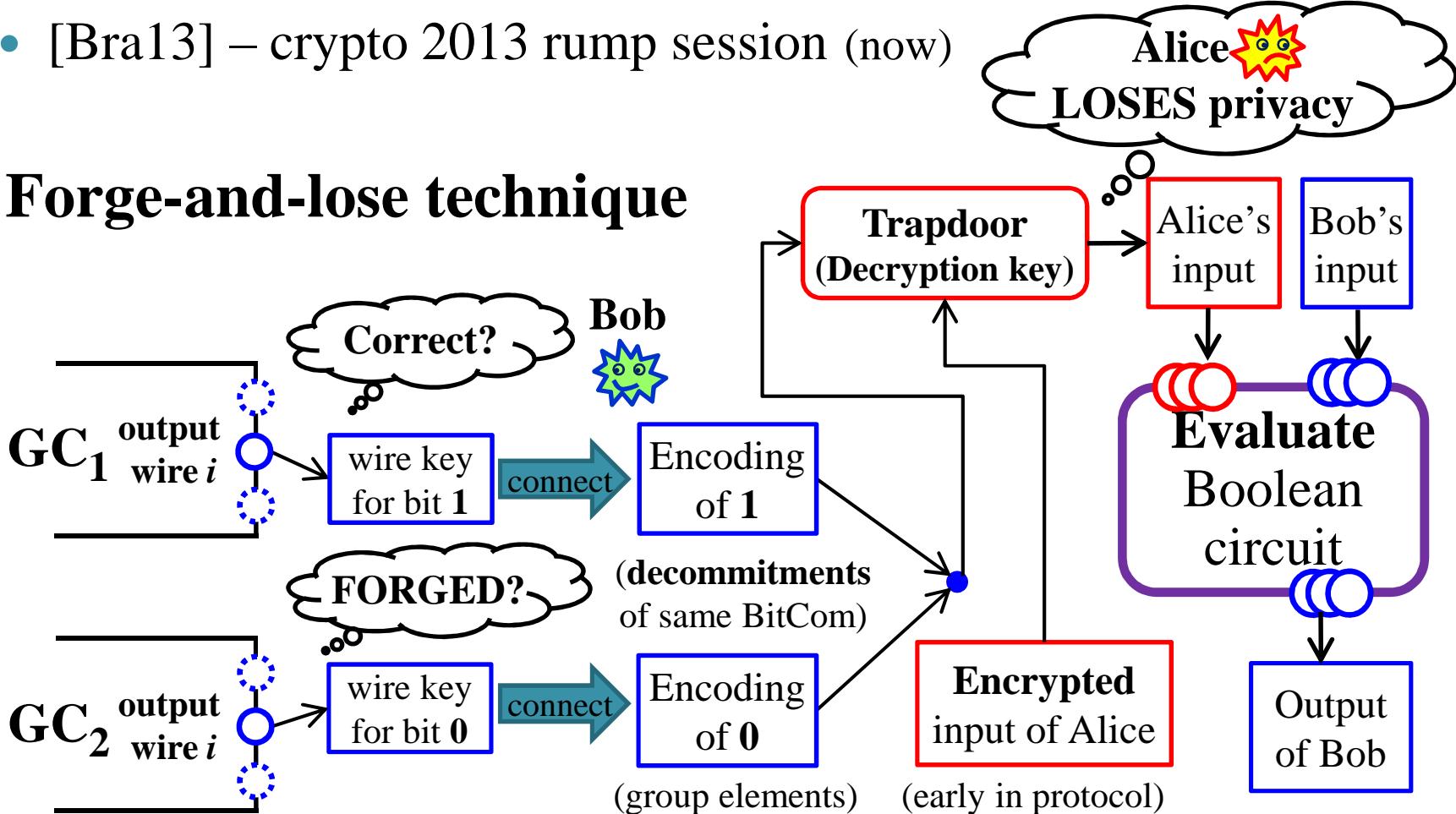
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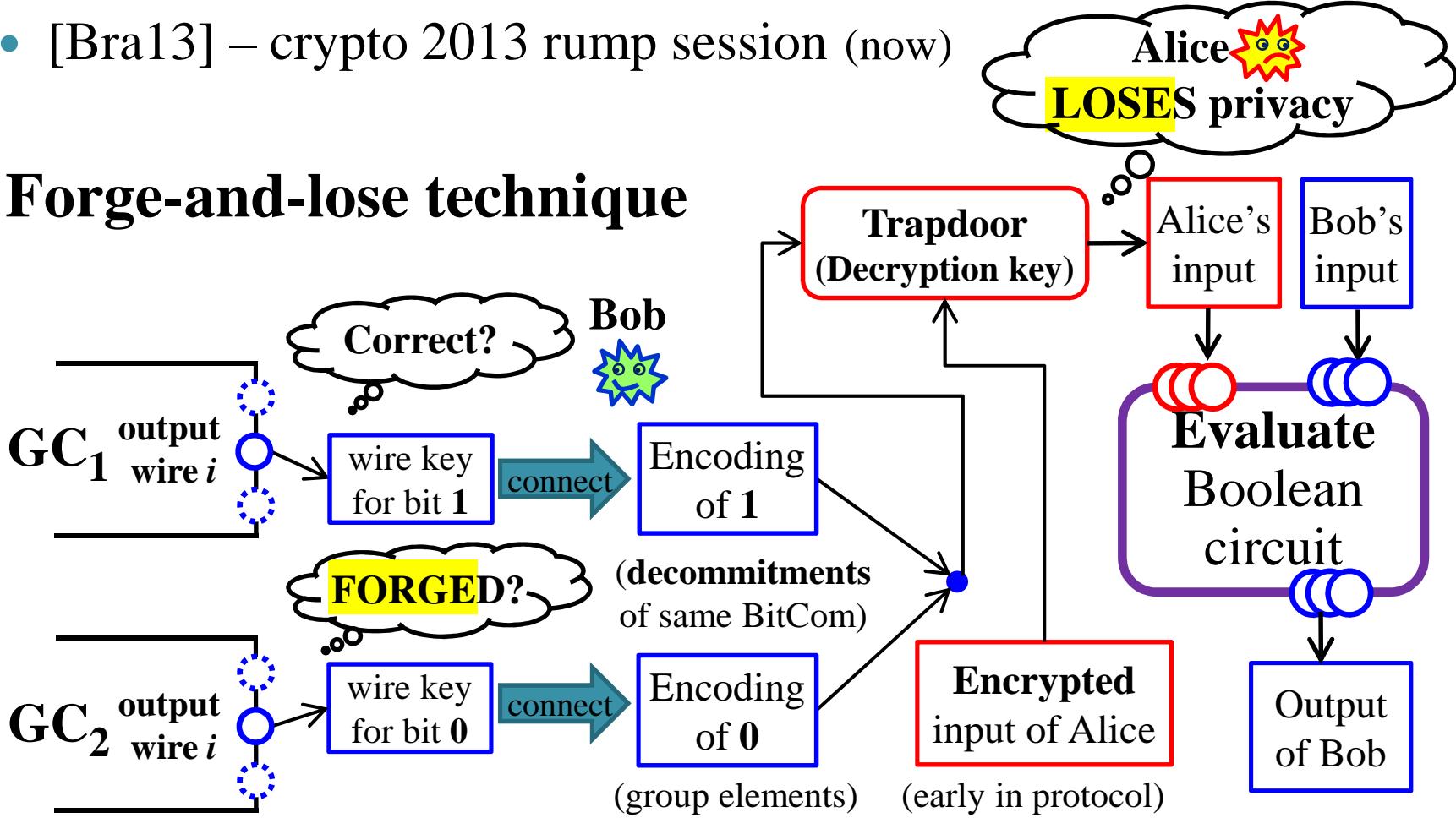
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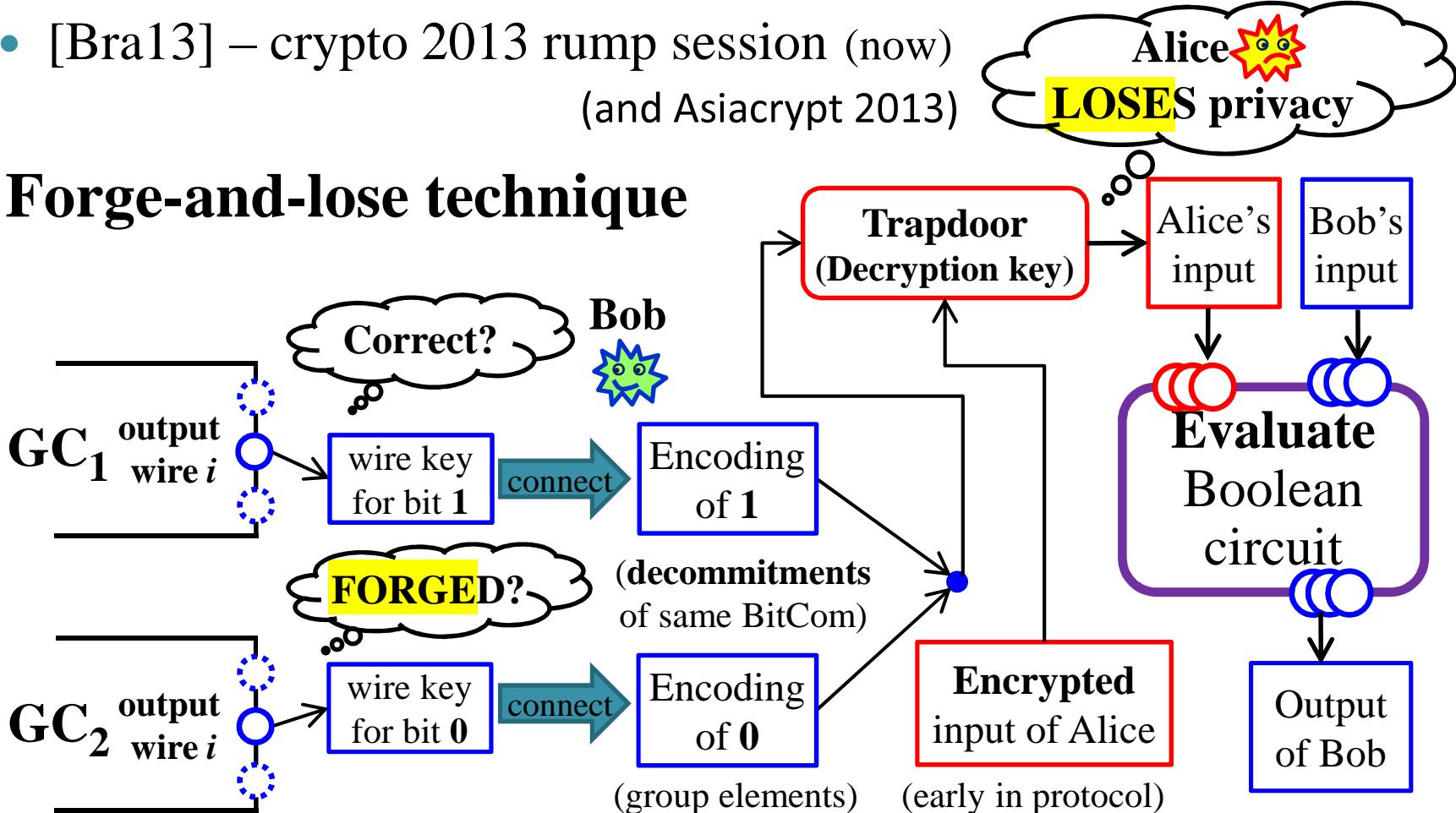
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# Benchmarking communication in F&L

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- Crypto security: 128 bits → 3,072-bit Blum integers [NIST-SP800-57]
- Statistical security: 40 bits ( $\Pr_{\text{error}} \leq 2^{-40}$ )
- Garbled gates: 384 bits
- Symmetric commitments: 256 / 384 bits

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$ C_\wedge $ [Bri13]		6,800
$l_A = l_B = l'_B$		128
$(l_A + l_B + l'_B) /  C_\wedge $		5.6%
s (# GCs)	41	123
Max # evaluation GCs	20	8
RSC@GCs	no	yes
GCs (Mb)	107	21
Total (Mb)	161	55
Overhead from non-GCs (%)	50%	163%

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- **#(exps):**  $O(l)$
- **Oblivious Transfers:** 2-out-of-1 OT
- **Proof security:** Ideal/real simulation  
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(Further optimizations on the way)

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# Thanks

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**lbrandao @ {fc.ul.pt, cmu.edu}**